

REMARKS/ARGUMENTS

Favorable reconsideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 7-14 are presently pending in this case. Claims 7 and 11 are amended by the present amendment. As amended Claims 7 and 11 are supported by the original disclosure,<sup>1</sup> no new matter is added.

In the outstanding Official Action, Claims 7-14 were rejected under 35 U.S.C. §102(e) as anticipated by Korean Patent Publication No. 0249783 (hereinafter “Korea ‘783”).

The outstanding rejection is respectfully traversed.

Amended Claim 7 recites in part:

a microprocessor for wired transmitting and receiving data to and from the IC card;  
an interface circuit for providing an IC card interface to allow the IC card to receive an electronic value from an external *server* by radio;  
an antenna; and  
*the microprocessor having a memory for temporarily storing a plurality of the electronic values and transferring the stored electronic values to another external IC card.*

Korea ‘783 describes a wireless mobile communication terminal with an embedded smart card chip.<sup>2</sup> The translated portions of Korea ‘783 describe communication between an internal interface and an *internal* contactless card.<sup>3</sup> The outstanding Office Action asserted that by using two terminals as described by Korea ‘783 and having them communicate with each other, the subject matter of Claim 7 is met.<sup>4</sup> However, as Korea ‘783 does not describe any terminals in contact with a *server*, it is respectfully submitted that Korea ‘783 does not teach “an interface circuit for providing an IC card interface to allow the IC card to receive an electronic value from an external *server* by radio.” Further, it is respectfully submitted that

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<sup>1</sup>See, e.g., the specification at page 6, line 36 to page 7, line 35 and Figure 3.

<sup>2</sup>See Korea ‘783, abstract.

<sup>3</sup>See, e.g., translated Claim 1 of Korea ‘783.

<sup>4</sup>See the outstanding Office Action at page 2, lines 16-23.

Korea ‘783 does not teach “the microprocessor having a memory for temporarily storing a plurality of the electronic values and transferring the stored electronic values to another *external IC card*.” Consequently, Korea ‘783 does not teach “an interface circuit” or “a microprocessor” as defined in amended Claim 7. Therefore, Claim 7 (and Claims 8-10 dependent therefrom) is not anticipated by Korea ‘783 and is patentable thereover.

Amended Claim 11 recites in part, “an interface circuit configured to provide an IC card interface to allow the IC card to send and receive an electronic value to and from external *server* by radio” and “wherein the microprocessor includes a memory configured to temporarily store a plurality of the electronic values and to transfer the stored electronic values to another *external IC card*.”

As noted above, even assuming *arguendo* that Korea ‘783 describes two terminals that can communicate with each other and include internal cards, Korea ‘783 does not describe communication with a server. Thus, Korea ‘783 does not teach “an interface circuit configured to provide an IC card interface to allow the IC card to receive an electronic value from an *external server* by radio.” Further, it is respectfully submitted that Korea ‘783 does not teach “the microprocessor includes a memory configured to temporarily store a plurality of the electronic values and to transfer the stored electronic values to another *external IC card*.” Thus, it is respectfully submitted that Korea ‘783 does not teach “an interface circuit” and “a microprocessor” as defined in amended Claim 11. Consequently, amended Claim 11 (and Claims 12-14 dependent therefrom) is also not anticipated by Korea ‘783 and is patentable thereover.

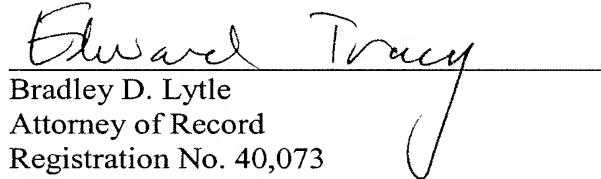
Application No. 10/516,312  
Reply to Office Action of September 17, 2007

Accordingly, the pending claims are believed to be in condition for formal allowance.

An early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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